

#3/PRE Amended  
5-2-86  
JAL

XA-9590

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Masao SHINOZAKI et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING A  
SEMICONDUCTOR DEVICE

\* \* \*

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified  
patent application as indicated below.

IN THE CLAIMS:

Please amend Claims 3, 4 and 7 as set forth below.

- 08  
At  
copy
- 1
  - 2
  - 3
3. (Amended) A semiconductor device according to Claim  
1, wherein a power supply voltage applied to the first MOS  
transistors constituting the input circuit or the output